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## A parallel embedded-processor architecture for ATM reassembly

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 Volume 7, Issue 1 (February 1999) [table of contents](#)

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↳ C.2.1 Network Architecture and Design

↳ **Subjects:** Asynchronous Transfer Mode (ATM)

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C. Computer Systems Organization

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S26	50	S25 and S24	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/28 09:42
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S38	18	S36 and S37	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/29 09:01
S39	25327941	@ad<"20040211"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/29 09:01

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S53	2297826	se	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/29 10:48
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S55	790789	segment\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/29 10:49
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